Electronic Acknowledgement Receipt

EFS ID:	6464932
Application Number:	10755042
International Application Number:	
Confirmation Number:	8665
Title of Invention:	Integrated chip package structure using silicon substrate and method of manufacturing the same
First Named Inventor/Applicant Name:	Mou-Shiung Lin
Customer Number:	89518
Filer:	Dennis Alan Duchene/Tobi Terry
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Attorney Docket Number:	085027-0104
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Application Type:	Utility under 35 USC 111(a)

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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		085027_0144_Amendment.pdf	87689 d2138d79d8c697a0738cf3411ef9913884a9 0217	yes	17

Multipart Description/PDF files in .zip description		
Document Description	Start	End
Amendment/Req. Reconsideration-After Non-Final Reject	1	1
Claims	2	8
Applicant Arguments/Remarks Made in an Amendment	9	17

Warnings:

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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
2	Drawings-only black and white line drawings	085027_0104_Replacement_Drawing_6_7.pdf	451838 ddbf16b812e0c9dba1361e0726eec88db0a 952f4	no	1

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